## Notice of References Cited | 10/665,998

Application/Control No.

10/665,998

Examiner

Eric B. Chen

Applicant(s)/Patent Under Reexamination SCHAADT ET AL.

Art Unit
Page 1 of 1

## **U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	Α	US-6,162,367	12-2000	Tai et al.	216/79
	В	US-4,498,953	02-1985	Cook et al.	438/738
	O	US-6,436,229	08-2002	Tai et al.	257/E21.218
	۵	US-6,558,559	05-2003	Becker et al.	216/2
	ш	US-			
	F	US-			
	G	US-			
	Н	US-			
	1	US-			
	J	US-			
	К	US-			
	L	US-			
	М	US-			

## **FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р	,				
	Q					
	R					
	s					
	Т	·				

## **NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, 1986, Lattice Press, Vol. 1, pp 386, 332-33, 399, 514, 520, 565-67.
	٧	Wolf, Silicon Processing for the VLSI Era, 2002, Lattice Press, Vol. 4, pp. 66-67.
	8	Rossnagel et al., Handbook of Plasma Processing, 1990, Noyes Publications, pp. 200-01.
	x	Ashurst et al., Journal of Microelectromechanical Systems, March 2001, Vol. 10, No. 1, pp. 41-49.

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.